wherein said frequency corrector comprises a feedback loop including a frequency offset detector for obtaining a measure of a frequency offset from said despread digital signal and a frequency correction generator for generating a frequency correction and a combiner for combining said frequency correction with said second signal to correct said frequency offset.

14. Cancelled.

15. (Previously presented) The receiver of claim 13, further comprising a timing circuitry communicatively coupled between the analog to digital converter and the down-converter to perform a timing correction function.

16. Cancelled.

17. (Currently Amended) The spread spectrum communications system of claim 6, further comprising a timing circuitry communicatively coupled between the analog to digital converter and the down-converter to perform a timing correction function.

18. Cancelled.

19. (Currently Amended) The spread spectrum communications system of claim 6, wherein said frequency correction is an up-sampled complex correction sequence $Z_{offs}(k)$, where k represents a given sampling instant, and where $Z_{offs}(k)$ is equal to 1 x exp $\{j\varphi_{offs}(k)\}$ where

$\varphi_{offs}(k)$ represents phase offset values at the first data rate which are linearly interpolated from an average phase difference at the third data rate.

20. (Currently Amended) The receiver of claim 13, wherein said frequency correction is an up-sampled complex correction sequence $Z_{offs}(k)$, where k represents a given sampling instant, and where $Z_{offs}(k)$ is equal to 1 x exp $\{j\varphi_{offs}(k)\}$ where $\varphi_{offs}(k)$ represents phase offset values at the first data rate which are linearly interpolated from an average phase difference at the third data rate.

21. (Currently Amended) The spread spectrum communications system of claim 6, wherein the RF signal receiver for generating the analog signal comprises the RF signal receiver providing the analog signal to the analog-to-digital converter.